Team Name: sdmay24-40
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Report Period: Aug 10-Oct 10

Summary of Progress in this Period

In this period, we have been working on determining the ADC part that we want to use in our project. We finished the schematic and most of the layout of the Evaluation Board for the radar IC. We had our GUI layout visualized with functionalities. We received the Alchitry FPGA to start learning the programming. We started on modelling the lens of the radar. In addition, we worked closely to determine the timeline of the project and more in depth of each person's tasks.

Pending Issues

We still have not finished deciding which flatform we want to make our GUI.
We have not reviewed our final layout for the Evaluation Board.
We have finished the ADC sorting and started the schematic for the Data Acquisition Board.

Plans for Upcoming Reporting Period

Our Evaluation Board should be ordered. The best scenario is that we have tested the board.
Our ADC schematic and layout should be done.
The lens modelling should be done.
We should start on making the front-ends and back-ends for our GUI.
We should be able to communicate between the FPGA and the PC.